

EPB-MPF

EPROM Programmer Board Operation Manual

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I. Introduction

The EPB-MPF is an EPROM programmer board for the MICRO-PROFESSOR, provides the programming capability for the following EPROMS.

1K x 8 2K x 8 4K x 8

TI : TMS2508, TMS2516, TMS2532,

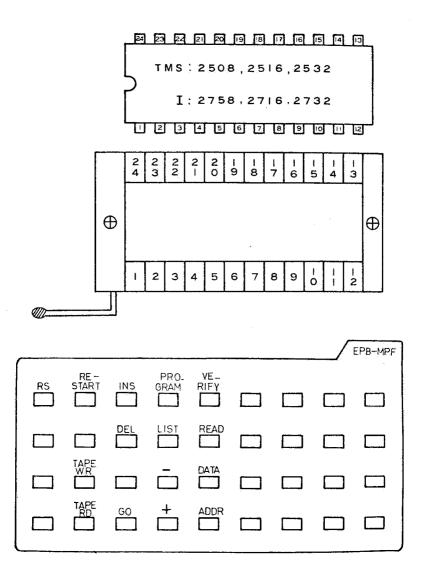
Intel: 12758, 12716, 12732,

Option: 275881865,

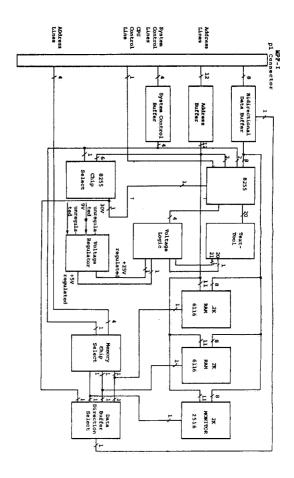
The EPB-MPF interfaces with the MPF-I by using a flat ribbon cable and utilizes the monitor for writing data to EPROM. Actually the EPB-MPF has two 40 pin male connectors -P1 and P2, P1 is to interface with the CPU BUS of MPF-I and P2 is for system expansion (for example, is to interface with SSB-MPF). Regarding the pin function of P1 and P2, please refer to 4-2. The MPF-I is usually covered by a name plate which has all kinds of keyfunctions, so it is quite convenient to operate the EPB-MPF.

Installation Procedure:

- 1. All power should be turned off.
- Connect the CPU BUS of MPF-I with Pl on the EPB-MPF by flat cable.
- 3. Plug in the MPF-I power plug.
- 4. Plug in the EPB-MPF power plug.
- Plug IC into textool, the indentation should face the left. (Please refer to next page)
- 6. As power adaptor output has no load, the voltage is 33V and 13.5V respectively; but the voltage is 30V and 10V when power adaptor output has load. Users can check whether the voltage of 723 pin 10 is 25.5V, if not, they can adjust VR 100K to 25.5V.
- To cover the EPB Name Plate on the MPF-I original keyboard location, then users can start operating.



II. EPB-MPF Block Diagram



III. EPB-MPF Specifications

3.1 Hardware Specifications

- Compatiable with MPF-I. Use 40 pin flat ribbon cable and male connector to interface with MPF-I.
- ROM: Single +5V EPROM 2516 x 1, total 2k bytes. Monitor EPROM Address: 9000 - 97FF.
- RAM: Static RAM 6116 x 2, total 4K bytes. Basic RAM Address: 8000 - 8FFF.
- 4. I/O Port: Programmable I/O port 8255x 1, total 24 parallel I/O lines.
 I/O address: CC CF.
- 5. Display: MPF-I display.
- 6. Keyboard: MPF-I keyboard compatible. Use MPF-I keyboard covered with a name plate to replace MPF-I keyboard name plate during EPROM programming operation.
- 7. Audio Tape Interface: MPF-I compatible interface.
- 8. System Power Consumption: +25V/30mA and +5V/350mA.
- 9. Main Power Input: +30V 75mA and +9V 400mA adaptor is provided, power adaptor input 110/220V.
- 10. Textool: 24 pin zero insertion force socket.
- 11. Interface Connector/Cable: 40 pin flat ribbon cable and male connector used to interface with MPF-I.
- 12. Extension Connector: 40 pin flat ribbon cable male connector provides the bus for CPU option.
- 13. Physical Characteristic:

Height: 1.60 cm Width: 11.15 cm Depth: 15.4 cm

3.2 Software Specifications

EPB-MPF has a high performance 2K-byte monitor program with 15 function keys and is designed for easy operation. The following is a simple description of the key functions:

		1							
1.	READ.	:	Read	data	from	EPROM	to	RAM	buffer.

2. | Verify EPROM with RAM buffer.

3. Lay : Display or modify data on RAM buffer.

4. Restart to initial state of EPB-MPF.

5. Write data from RAM buffer to EPROM.

6. Store data in RAM buffer onto the cassette tape.

7. TAPE : Load data from the recorder.

8. cal : Delete 1 byte from data of the current display address of RAM buffer.

9. Insert 1 byte into data of the address following by the current display address by RAM buffers.

10. + : Check contents of next memory address.

11. : Check contents of last memory address.

12. Execute the keyfunction of EPB-MPF.

: System Reset. The display shows UPF--1 controlled by the monitor of MPF-I but not the monitor of EPB-MPF.

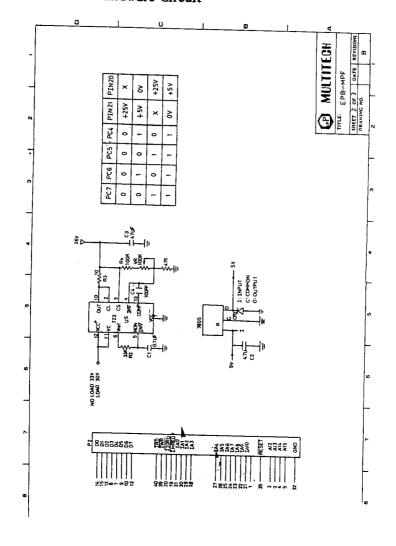
14. Set the address of RAM buffer.

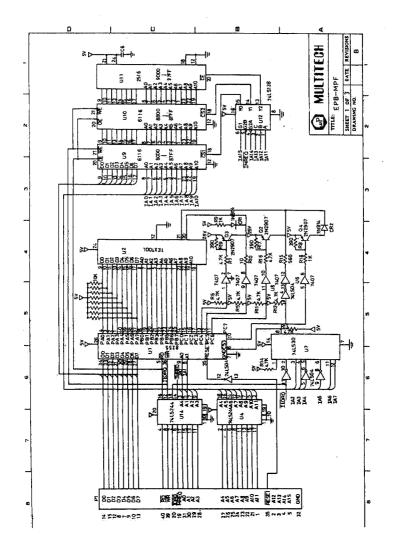
15. OATA : Input data to RAM buffer.

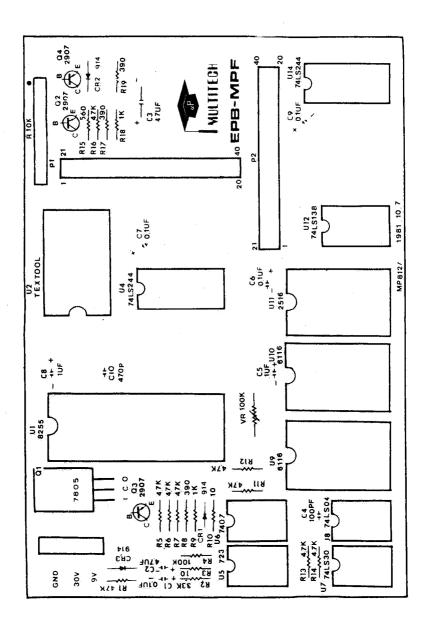
16. NEW : Set new EPROM type.

IV. Theory of Hardware Circuit

4.1 EPB-MPF Hardware Circuit







4.2 EPB-MPF Pin Function P1 & P2

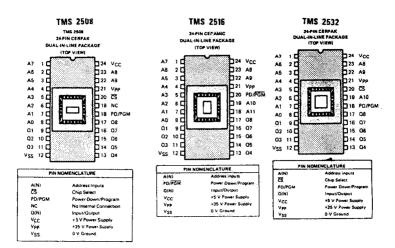
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1																				MULTITECH	EPB-MPF	SHEET 3 OF 3 DATE REVISIONS DRAWING NO. D	
N																			ļ	3	THE	SHEET	
7																							-
6	NOI	PIN NO SIGNAL	IAIO	186	IA8	IA7	146	IAS	Į.	EA3	IA2	Ā	IAO	GND	RFSH	ī	RESET	BUSA	MAT	BUSAK	IWR.	180	,
-	PIN FUNCTION	PIN	12	22	8	77	25	32	23	28	53	30	Æ	32	33	34	35	36	37	38	8	9	-
*	P2 PIN	SIGNAL	IAI	Aiz	AJ3	A14	Ais	161	å	8	22	ద	ž	20	20	8	ā	E	N.	HALT	D. BARCO	IIORO	
1		PIN NO		2	e,	-#	2	٠	7		6	0.	=	22	52	71	15	92	17	80	61	20	-
s	•	\	·	·····							l	ļ	L.,,_,,,,,	L	Ļ	L	L	l	<u> </u>	l	<u> </u>		,
-	NO.	SIGNAL	A10	49	48	Α7	A6	AS	A4	А3	A2	Ą	AO	GND	RFSH	Σ	RESET	BUSRO	WAIT	BUSAR	¥.	RD	-
9	PIN FUNCTION	PIN NO	21	22	23	77	52	26	27	28	53	30	31	32	33	3,4	35	8	37	38	65	0.7	۵
-	P1 PIN	SIGNAL	A11	A12	A13	A14	A15	164	7.Q	D3	0.5	90	NC.	D2	70	8	5	ĪNĪ	NMI	HALL	MREO	10RQ	-
,		PIN NO	-	2	3	,	5	9	7	8	6	01	11	12	13	14	15	16	17	18	19	20	۲.
+																							-
8																							

4.3 Theory of Operation

EPROM is the abbreviation for Erasable Read Only Memory which is a kind of Read Only Memory that can write data and rewrite new data into it. The method of clearing the contents of EPROM is to put it under a ultraviolet ray source about 20 to 30 minutes. Each kind of EPROM has its own pin function, and there are three kinds of voltage OV. +5V. +25V are needed for EPROM programming. the voltage logic of EPB-MPF must support different voltage to the same pin for different type of EPROM. By the hardware circuit users will observe that Ul (8255) has three ports: PAO-PA7 controls data bus, PBO-PB7 and PCO-PC3 control address bus, but PC4-PC7 controls the voltage logic to provide the different voltage for pin 21 and pin 20 with the different EPROM type.

For example, the process of writing data into a single 5V 2716 (or 2516), is adding an extra 25V source on its pin 21 (Vpp) and supplys both address and data, then a byte is written in. Continuing this process until all the bytes are programmed. Time is very critical during the process, if it is not properly controlled, it would cause the damage of 2716.

4.4 The Pin Funciton and Operation Voltage



TMS 2508 operation

	Į		MODE									
FUNCTION	PN	READ	OUTPUT DISABLE	POWER	START PROGRAMMING	INHIBIT PROGRAMMING	PROGRAM VERIFICATION					
PD/PGM	18.	VIL	Don't Care	Vitt	Pulsed Vil. to Vin	V _{II}	Vu					
ČS.	20	V _{IL}	ViH	Don't Care.	VIH	ViH	Vit					
VPP	21	+5 V	+5 V	+5 V	+25.V	+25 V	+25 V (or +5 V)					
VCC	24	+5.V	+5 V	15 V	+5 V	+5 V	+5 V					
Q	9-11, 13-17	0	HI-Z	HEZ	D	HI-Z	0					

TMS 2516 operation

FUNCTION	MODE										
(PINS)	Read Output Orable		Power Down	Start Programming	Intituit Programming	Fragram Verification					
FD/FGM (18)	VIL	Don't Care	ViH	Pulsed V _I L to V _{IM}	VIL	VIL					
(20)	Ψ _{IL}	ViH	Don't Care	VIH	ViH	V _{IL}					
Vpp (21)	+5 V	+5.V	•5 V	+26 V	+25 ∨	+25 V (or +5 V)					
VCC (24)	+5 V	+6.V	+5 V	+5 V	+\$.V	+5 V					
Q 19 10 11 .	a	HI.Z	Hi Z	Ö	ньг	0					

TMS 2532 operation

FUNCTION	MODE									
(PINS)	Reed	Output Disable	Power Down	Start Programming	Inhibit Programming					
PD/PGM (20)	ViL	VIH	Vivi	Pulsed V _{IH} to V _{IL}	VIH					
(21)	+5.V	+5 V	+6 V	+25 V	•25 V					
VCC 1241	+5 V	+5 V	15 V	+5 V	+5 V					
Q (9.16.11, 13.16.17)	0	HITZ	HI-Z	D	H) Z					

2758

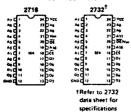
PIN CONFIGURATION



MODE SELECTION

PINS	CE/FGM (SII)	An trei		V,,,,		.0UTPUTS (6-11, 13-17)
Reed	V _R	V _n	V _H	+5	+5	Dout
Sundby	٧	Vi.	Don't	+5	+5	High Z
Program	Pulsed VIL 10 VIN	Vn.	V.,	+25	-5	D _{av}
Program Verity	I Vn	I Va	VIL	+25	1 15	Dout
Program Inhibit	V _k	I Va	Vin	+25	+5	High Z

PIN CONFIGURATION



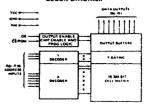
MODE SELECTION

MODE	614) (14)	53 WE1	Very Egrii	Vec (34)	DUTPUTS (\$-11, 13-17)
Fired	V _{IL}	VIL	**		001/7
Surelley	Ville	Dan's Care	-4		Hugh Z
Program	Parlend VII. 19 Viet	VIN	+29	**	D _{ph}
Program Verify	YIL	YIL	+26	-	Pour
Program inhibit	Vit.	Yu	- 26	**	Hep- Z

PIN NAMES

49- 418-	ADDARSES
CI PGM	CHIP BRABLE PROGRAM
Ø.	CUTPUT SHABLE
0,-0,	OUTFUTS

BLOCK DIAGRAM



2732

PIN CONFIGURATION

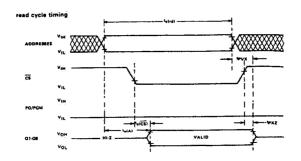


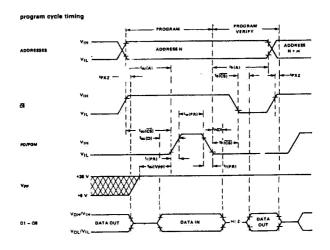
MODE SELECTION

PINS	CE (18)	ŌΈΛ/ _{PP} (20)	V _{CC} (24)	OUTPUTS (9-11,13-17)
Read	VIL	Vil	+5	Dour
Standby	VIH	Don't Care	+5	High 2
Program	VIL	Vpp	+5	Din
Program Verity	Vit	VIL	+5	Dovt
Program Inhibit	ViH.	Vpp	+5	High Z

The following explanation serves the theory of writing data in I2716 which is the pin function compatiable with TMS 2516. The method of writing I2716 is to put +25V on the Vpp (pin 21), convey the essential address or data, and transfer the 50ms cycle to the CE/PROG (pin 20). After handling these procedures, users first write 1 byte and then continue one by one.

The read cycle timing as well as the program cycle timing of 12516 are shown below:





In view of the circuit, Ul (8255) is divided into three ports: PAO·PA7, PBO-PB7, and PCO-PC3 to control the keyfunction of textool. Besides, PC4-PC7 can control the voltage logic to provide the different voltage for pin 21 and pin 20 with the different EPROM type.

Since these EPROMs operate from a single +5V (in the read mode), they are ideal for use in microprocessor system. One other (+25V) supply is needed for proramming but all programming signals are TTL level, requiring a single 50ms pulse. Locations may be programmed singly, in blocks, or at random. Total programming time for all bits of 12716 or TMS 2516 is 100 seconds.

Note: Avoid taking off EPROM or pressing the while EPB-MPF is at program cycle timing.

There are three parameters must be set before executing PROGM command. Suppose they have any errors, press key and then -Err is shown.

- 1. S Store the starting address of the data which will be written into ${\tt EPROM}$
- E Store the ending address of the data which will be written into EPROM.
- D Store starting address of the EPROM where the data will enter.

There are four conditions as the value of parameter is wrong:

- The ending addrss (E) is smaller than the starting address (S).
- 2. The data length (E S + 1) is larger than 800H (for 2K) (400H for 1K, 1000H for 4K)
- The starting address of EPROM (D) is equal or larger than 800H (for 2K). (400H for 1K, 1000H for 4K)
- 4. The ending address of EPROM (D + E S) is equal or larger than 800H (for 2K). (400H for 1K, 1000H for 4k)

Once any errors occur, the display will show Err.

V. General Conception

5.1 Function of Monitor Program

Before executing the keyfunction of EPB-MPF, user should enter the EPB-MPF monitor program. As the address of the EPB-MPF monitor is 9000 - 97FF, the way to enter the EPB-MPF is to set the starting address on 9000 and then press we key. When MPF-I display shows 0.0.0.0.E, it means the EPB-MPF monitor already starts and is ready to input the EPROM type. MPF-I keyfunction, which has lost its original use, has been replaced by the EPB-MPF keyfunction. Users just need to press if they want to return to the MPF-I monitor.

The monitor function is indicated as follows:

- Read the data from EPROM to RAM buffer, users can store, modify, verify, duplicate, and display it.
- Input the data of RAM buffer to recorder, or read the data from recorder to RAM buffer.

Example: Read the data of 2716 to RAM buffer and then verify it.

(Put EPROM on textool before reading data.)

Key	Display	Comments
RS	UPF1	;System reset
ADDR IV AF O AF	9.0.0.0.31	;Set the starting address of EPB-MPF
		monitor program.
<u>ω</u>	0.0.0.0E	;Execute the EPB-MPF monitor.
DE 3. 1 DE 6	2.7.1.6E	; Key in the EPROM type.
60	2716-E	;The initial state of EPB-MPF.
READ	r E Ad	; Key in the READ command
ω .		Executing the read command, the display is blank.

The end of read command. PASS - -VER ;The monitor sets the 0.0.0.0.-sstarting address on 0000 and the ending address on O7FF automatically. Users can verify the data of any region through setting both the starting address and the ending address. 90 ;Execute the verify command. PASS-H ;All data are matched, showing PASS-H.

[Description]

- Users should execute the keyfunction in the initial state of EPB-MPF, it stands for controlling by the EPB-MPF monitor. The display will show 2508-E, 2758-E, 2516-E, 2716-E.
 2532-E, or 2732-E, as EPB-MPF is in the initial state, it expresses the EPROM type has been confirmed and users can start to execute the keyfunction at this moment.
- Suppose users don't put EPROM on the textool, the data reading from textool all shows FF.
- 3. Press ** key in case users intend to return to the initial state.
- If users try to change the EPROM type, they must press key and reset the EPROM type.
- Press the keyfunction directly if command has finished and users are ready to execute the new one.

5.2 RAM Addressing

Key

Though the addresses of RAM buffer are 8000 - 8FFF, we make use of software technique to cause the display show 0000 - 0FFF instead of 8000 - 8FFF. All of these designs are for user's convenience. For example, it means the value of 8356 in RAM addressing is 38 when the display shows 03563.8. So the addresses of 8000 - 8FFF in MPF-I are the same as the addresses of 0000 - 0FFF in EPB-MPF. Now the following example could prove the both area's addresses are similar.

Example: Compare the addresses of 0068 in EPB-MPF with the addresses of 8068 in MPF-I.

Display

System Reset. System Reset	ice y	= = -,*,	
8 0 8 18 XX. 8068 is XX. 1	ms	U P F 1	;System Reset.
8068 is XX. 8068 is X. 8068 is XX. 8068 is XX. 8068 is X. 8068 is XX. 8068 is X.	AGOR IX AF DE IX	8.0.6.8.××	;The content of address
EPB-MPF monitor. CO			8068 is XX.
CONT	AGON IN AF AF O	9.0.0.0. 31	
is ready to accept the EPROM type. 2.5.0.8E ; Set the EPROM type on 2508. 2.5.0.8-E ; The initial state of EPB-MPF. [INT]	السالسالسالسا	him and the second second	EPB-MPF monitor.
is ready to accept the EPROM type. 2.5.0.8E ; Set the EPROM type on 2508. 2.5.0.8E ; The initial state of EPB-MPF. ; Key in the list command. co 0.0.0.0.x. ; The first address in RAM buffer. ; The six-eighth address in EPB-MPF monitor equals to the address of 8068 in		0.0.0.0E	The EPB-MPF monitor
Set the EPROM type on 2508. Set the EPROM type on 2508.		Lippon	
2508. 2508. (The initial state of EPB-MPF. (Key in the list command. (O000×.×.) (The first address in RAM buffer. (The six-eighth address in EPB-MPF monitor equals to the address of 8068 in			EPROM type.
2508. 2508. 2508-E ;The initial state of EPB-MPF. ;Key in the list command. 60 0000×.×. ;The first address in RAM buffer. 60 10000×.×. ;The six-eighth address in EPB-MPF monitor equals to the address of 8068 in	DE BC AF IX	2.5.0.8E	:Set the EPROM type on
EPB-MPF. LIST ;Key in the list command. command. in EpB-MPF monitor equals to the address of 8068 in	لتالتالتا	<u></u>	2508.
EPB-MPF. LIST ;Key in the list command. command. in EpB-MPF monitor equals to the address of 8068 in	[]	2508-5	The initial state of
command. $\begin{array}{c} & & & & & & & \\ & & & & & & \\ & & & & $	ليًا	2.5 0.8 - E.	
command. $\begin{array}{c} & & & & & & & \\ & & & & & & \\ & & & & $	السنسا		
$ \begin{array}{c} \text{CO} \\ \text{ADOR} \\ \text{O} \\ \text{O}$	LIST	LEST	
RAM buffer. $0.0.0.0.\times\times$; The six-eighth address in EPB-MPF monitor equals to the address of 8068 in			command.
Acon $ \begin{array}{ccccccccccccccccccccccccccccccccccc$	60	0000×.×.	The first address in
The six-eighth address in EPB-MPF monitor equals to the address of 8068 in			RAM buffer.
in EPB-MPF monitor equals to the address of 8068 in		0.0.0.0.××	The standarder addrage
to the address of 8068 in	AF O O O O O O O O	0.0.6.8.××	
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			The Control of the Co

Comments

VI. Operation Introduction

The EPB-MPF keyfunctions should be operated under the monitor's control. After executing the addresses of 9000 in EPB-MPF monitor, the display will show 0.0.0.0. -E and users can key in the EPROM type. If any wrong types are shown, users press - key and the display becomes 0.0.0.0 -E. But the display will show xxxx -E in case of no errors. As EPB-MPF is in the initial state, the display will show 2508-E, 2758-E, 2516-E, 2716-E, 2532-E, 2732-E, etc. Pressing - key will not affect the value of RAM. Users may reset the EPROM type and re-execute the EPB-MPF monitor if they intend to change the EPROM type.

Once the EPB-MPF monitor starts to execute command, EPB-MPF monitor will examine U9, U10, and U11 whether they have any problems. The display shows bAdU09 if U9 (the address of 8000 - 87FF in RAM) is out of order; moreover, bAdU10 is expressed when U10 does not function very well. But bAdU11 or RADOM DATA are shown if U11 has some troubles.

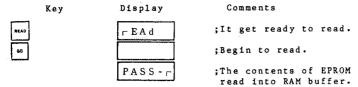
[Description]

- Read data from EPROM to RAM buffer to store, modify, verify, duplicate, display etc.
- Users can use LIST command to modify or display the data in RAM buffer.
- We use VERIFY command to compare the data of EPROM in the socket with RAM buffer of the same address.
- It also can use PROGRAM command to duplicate an EPROM.
- When reading is completed, the display will show PASS-r. User can execute LIST command to check whether the data in RAM buffer is correct or not.

6.1 Read data from EPROM to RAM buffer- Key

The following procedures guide users how to implement the read command which can read the data from EPROM in the textool to RAM buffer.

- Users should put EPROM in the textool before executing the read command, or the data reading from textool will be FF.
- 2. Before pressing | MEAD | key, users need to make sure whether the EPB-MPF is in the initial state that means | xxxx-E | is shown.
- When the display is in the initial state, the other step is shown as follows:



Example: The EPROM type on textool is 2532, reading its contents into RAM.

Key	Display	Comments
ADDR Y AF O AF	9.0.0.0.31	;Set the starting address of EPB-MPF monitor program.
. 60	0.0.0.0E	;Wait to key in the EPROM type.
DE 8C M. DE 2	2.5.3.2E	;The EPROM type is 2532.
<u> </u>	2532-E	;The initial state of EPB-MPF.
READ	r E A d	;Set READ mode.
60		Execute the READ command.
	PASS-F	The READ command is finished.

[Description]

- In case the EPROM type is 2508 or 2758, it means to load in the 1K bytes data. The situation is similar in 2516, 2716(2K) and 2532, 2732(4K).
- After completing the READ command, users can utilize LIST command to display or modify the data in RAM buffer.
- 3. Check whether EPROM is blank or not. At first, don't put EPROM onto textool and the loading value will be FF. Now put EPROM onto textool, press were and .

6.2 Display and modify the data in RAM buffer - Key

Six keyfunction are used with the key to display or modify the data in RAM buffer.

- 1. Substitute memory ADDR and DATA key.
- 2. Data deletion | key.
- 3. Data insertion ws key.
- 4. Check the next memory address + key.
- 5. Check the last memory address - key.

Users should take the following steps to display or modify data in RAM buffer.

- 1. The EPB-MPF must be in initial state before pressing the $\begin{picture}(t) \put(0,0){\line(0,0){100}} \put(0,0){\line(0$
- If it is in initial state, do the steps as follows.

Key

Display

Comments

LIST

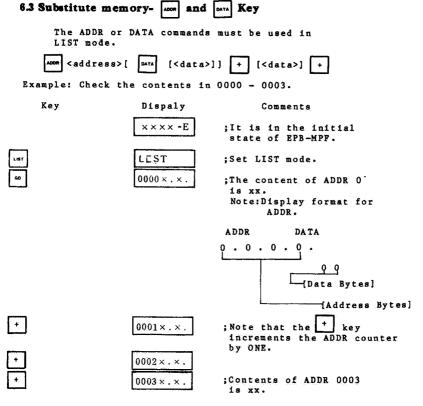
LEST 0000×.×.

;Set LIST mode

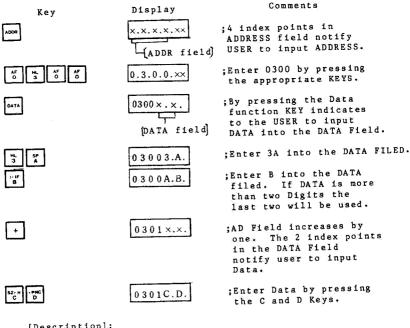
; It always displays the first byte of data in RAM buffer. Example: Display the contents of memory locations.

Start the address at 0586 and end the
address in 0588. Change the contents of 0587
into 56.

Comments Display Key ;Set the starting address UPF - - 1 of EPB-MPF monitor progarm. 9.0.0.0.31 0.0.0.0.-E The EPROM type is 2516. 2.5.1.6. -E ;The initial state of 2516-E EPROM monitor. :Set LIST mode. LEST 0000×.×. The first byte of data in RAM buffer. The four index points $0.0.0.0. \times \times$ notify user to input address. ;The contents of address 0.5.8.6.xx0586 is XX. The contents of address 0587×.×. 0587 is XX. :The contents of address 0588× .×. 0588 is XX. $0587 \times . \times .$ 05875.6. :Change the contents of 0587 into 56. [Description]: , and only used key, and then the value of address 0000 will be shown.



Example: Continue the above example. Change the contents of 0300 into AR, 0301 into CD.



[Description]:

Addr means address. After pressing this key the display is in the standard format, i.e. the left four digits stand for the address and the right two digits stand for the data. The address field is indexed by four points and required 4 digits. If more than 4 digits are keyed in, only the last 4 are accepted. If less than 4 digits is entered, the address being displayed is assumed.

is pressed, the index points will be shifted to the rightmost two digits notifying the user to enter data. The content of RAM will be replaced by the entered data. Pressing • or will increase or decrease the address field. If the index points are already in the data field then it is unnecessary to press , the user may press + . After pressing or directly.

If the user attempts to change the contents of ROM, the display will blank out. After releasing the key, the display will return.

6.4 Data insertion- Key

The INS command must be used in LIST mode.

* <data>

When the display is of the Addr-Data form, the input data will be inserted after the displayed address.

Example: Assume the contents of RAM are as follows:

	ADDRESS	OLD DA	TA DATA	AFTER	INSERTION
insert 33 here-	0200 0201 0202 0203 0204 0205	00 11 22 44 55	!	00 11 22 33 44 55	
Key	display		Co	mments	
	××××-E		The in of EPB	itial s	tate
	000 × . × .		;To cha		display to
	02030.0.		the ad insert ;Insert 0202,	dress o	e after field
HL 3	2033.3.		in dat		acy.
	0.2.0.0.00 02011.1. 02022.2. 02033.3. 02044.4.		Check.		

[Description]:

The valid region for this key is the same as ___ . After insertion, the last byte of the inserted block is lost.

The inserted address is one byte after the displayed address. Pressing this key causes all the data after the displayed address to be shifted down one position. Then the address field is incremented by one and the user may enter the data he wants to insert.

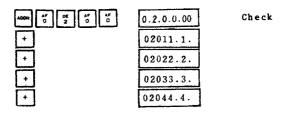
6.5 Data deletion - Key

The DEL command must be used in LIST mode.

This key is valid when the display is of the Addr-Data form. Pressing this key causes the data of the displayed address to be deleted. All the data below this address is shifted up one position.

Example: Assume the present contents of RAM and the desired contents are as follows:

	ADDRESS	OLD	DA TA	DATA	AFTER	DELETI NG
	0200	.0	0		0.0	
	0201	1	1		1.1	
delete address ->	0202	1	1		22	
	0203	2	2		3.3	
	0204	3	3		4.4	
	0205	4	4		ХX	
Key	Display	•		Comm	ients	
	××××-	E		e init EPB-M	ial st	ate
LIST	UEST 0000×.×		;Se	t LIST	mode.	
ADDR AF DC 2	0.2.0.2.1		t o	the A	ddr-Da	display ta form address
OKL	02022.2.	J	to The O26 and	be de e old D2 hav d data	leted. conten e been below	ts of deleted it has
			n e v 2 2 ,	v cont	fted u ents o h was of 02	f 0202 is the original



[Description]:

Data in ROM can not be deleted. The valid regions for this key are 0000 - 0FFF. When the deleted address is between 0000 - 0FFF, all the data after this address shift up one position. The last one 0FFF is filled with 0.

6.6 System reset - Key

Pressing the reset buttom will display UPF--1 and get back to MPF-I monitor program.

There are two possible results. When the MPF-I monintor is reset.

1. Power on

- a. Disable interrupt (IFF set to 0);
- b. I register set to 0;
- c. Interrupt mode set to 0;
- d. user's PC is set to 1800.
- e. User's SP is set to 1F9F;
- f. Break point is disabled.
- h. MPF-I is displayed one character at one time from right to left.

2. Press Ms

(a) - (e) are the same as (i). The contents of IFEE & IFEF and break point are unaffected. 'UPF--I' is dislayed (all digits) simultaneously.

6.7 Verify the data of EPROM with the data of RAM buffer- Key

vam <address> + <address> ••

Verify command can compare the data in EPROM with the data in RAM buffer. If the starting address or the ending address has no change, the EPB-MPF monitor will automatically set and verify the data of EPROM. For example, the EPROM type is 2716 and then the EPB-MPF monitor will set the starting address on 0000, the ending address on 07FF.

Example: Compare the data of 0150 - 0250 in RAM buffer with the same address of EPROM in the socket.

Key	Display	Comments
	××××-E	The initial state of EPB-MPF.
VERI	0.0.0.0S	;S is the menmonic of starting address in RAM buffer.
AF SC SC AF O	0.1.5.0S	;Set the starting address on 0150.
⊡	x.x.x.xE	;E is the mnemonic of ending address.
AF UE BC AF 0	0.2.5.0E	;Set the ending address on 0250.
60		Execute the VERIFY command.
	PASS-H	; VERIFY finished.

[Description]:

- Write the data of RAM address 0100 0250 into EPROM, so the starting address of EPROM where the data will enter is 0000.
- 2. The command may compare the data of EPROM in the socket with the same address of RAM buffer. If the result does not match, the display will first show the address in RAM buffer which does not match and then the display is blank for a while, showing E.xxd.xx. It means the left three LED is the data of EPROM and the right three LED is the data of RAM buffer. Press key and continue this process. The display will show PASS-B if all the data matches. The rest data can be deduced accordingly.
- Set the starting address, the ending address beforehand and key in the data, if users want to compare data in blocks.
- 4. Suppose the command loads in the wrong parameter that is to say E<S, the display appears -Err .

6.8 Return to the initial state of EPB-MPF- Key

Press key at any time, the display will show xxxx-E which means EPB-MPF is in the initial state. If the command is accomplished and is to do another one, users can press key directly.

Example: Read and verify the data of 2532 in EPROM, and then return to the initial state of EPB-MPF monitor.

Put 2532 into textool before pressing key.

L		
Key	Display	Comments
PS PS	UPF 1	
ريات احتالت		The starting address
ADOR O AF O	9.0.0.0.31	of EPB-MPF monitor.
[]	0.0.0.0E	:The EPB-MPF monitor
	0.0.0.0	is ready to accept
		the EPROM type.
DE 80 HL DE 2 5 3 2	2.5.3.2E	The EPROM type is
	2,0,0,0,0	2532.
[]		The initial state of
<u>ω</u>	2532-E	EPB-MPF monitor.
READ	r EAd	; Set the READ command.
		:Execute the READ
60 E		command.
		command.
	PASS-	:The READ command is
		finished.
VERI FY	0.0.0.0S	;Set the VERIFY
		command.

60		;Execute the VERIFY
L	L	command.
		m1
	PASS-H	The VERIFY command is finished.
		rinished.
RESTA	2 5 3 2 - E	The initial state of
	2002 1	EPB-MPF monitor.
		BIS THIE MONITOR.

[Description]:

- 1. Read the data of 2532 in EPROM to RAM buffer and verify, so the result must be same and then |PASS-H| appears.
- Once users take off EPROM and press the data will show FF.
- If users want to modify the data of 0532 in RAM buffer and then find it out in the EPB-MPF monitor, please use the LIST command.
- 4. As Err is shown, pressing key can return to the initial state of EPB-MPF monitor.

6.9 Write the data in RAM buffer to EPROM- Key

The primary function of to write data in RAM buffer at one time. The procedure is as below: <address> + <address> + <address> • Comments Display Key ;The initial sta ×××× **- E** of EPB-MPF monitor. ; Notify users the x, x, x, x, -Sstarting address to write in RAM buffer. ; Notify users the ending x.x.x.x.-E address (O3FF for 1K, O7FF for 2K, OFFF for 4K) to write in RAM buffer. : Notify users the address x.x.x.x.

which EPROM starts to write.

Example: Write the data 0200 - 0250 in RAM buffer to EPROM in its address 0000. Make sure the EPROM address must be blank (FF). In case the purpose is only for test, users don't need to put EPROM onto textool but make the value in RAM buffer be FF.

the value	In KAM Duller b	e rr.
Key	Display	Comments
•	××××-E	;The initial state of EPB-MPF monitor.
## OE OF	X.X.XS 0.2.0.0S X.X.XE 0.2.5.0E X.X.Xd 0.0.0d XXXXX.	;Start to write ad display the data or address which is proceeding. ;The last data and address in writing
		procedure.

[Description]

- The EPB-MPF monitor still execute the PROGM command, though EPROM is not on the textool. Users can check whether the Vpp pin of textool has 25V voltage. Anyhow the different EPROM types have the different pin functions.
- Attention: Don't put EPROM onto textool or take EPROM from it as the command is proceeding.
- 3. The display shows Err if the loading parameter bring forth any mistakes (Please refer to 4-4). Press key, the command will go back to the initial state of EPB-MPF monitor.
- 4. Before executing command, EPB-MPF will check whether the EPROM is blank or not. As long as data exists, FUII is shown. No matter what the value happens, you can press key if you intend to continue writing the data.
- 5. The EPB-MPF monitor in executing is not only writing the data but checking the value. If EPROM is out of order, naturally the data could not write into it. At this moment, EPB-MPF sounds and then the display shows xxxxHH. "xxxx" stands the address in EPROM is out of order, but "HH" is to notify users that a bad address exists and they can press key if they continue writing data. Refer to 2732, the new monitor is to be examined after writing but not be checked one by one as writing the data. The data of origin monitor is located in 9000H 9712H, but the data of new version is 9000H 9783H
- 6. If we want to write only one byte, then the values for S and E must be the same. Moreover, if we want to change one bit of this byte, all we have to do is to press key after the pattern "FULL" is shown on the display. (Notice that the bit can only be changed from 1 to 0)
- 7. Note: Suppose in the initial state of EPB-MPF users test the voltage of pin 20 or pin 21 on textool is 25V, it indicates there is something wrong at transistor Q2, Q3, or 7407. Users must stop operating the procedure but change transistor Q2, Q3, or 7407 to avoid damaging EPROM.

6.10 Storing data onto tape- Key

EPB-MPF takes advantage of the audio tape interface in MPF-I to store the data of RAM buffer onto tape.

Cassette tape is a large capacity non-volatile storage medium. MPF-I contains Lardware and software drivers.

TAPK wm <file name> + <address> + <address> \bigcirc

Example: Store the data of 0000 - 0100 on tape, use 1234 as file name.

Key	Dispaly	Comments
	$\times \times \times = E$	The initial state of EPB-MPF.
TAPE	\times , \times , \times , $-$ F	F is the mnemonic of filename.
#C OE HL AF	1.2.3.4F	Filename = 1234
\Box	×.×.×S	;S is the mnemonic of starting address.
6 6 6	0.0.0.08	;Stating address = 0000.
	×.×.×E	E is the mnemonic of ending address.
	0.1.0.0E	;Ending address = 0100
(PLAY & REC)		Connect the microphone of the tape recorder to MPF-I MIC. Start record-
60		ing by pressing PLAY and REC key of recorder.
		Begin to output data. During transfer the display is dark, but the TONE-OUT LED is on.
	×××××.	;When transfer is completed, the ending address is
		displayed.

[Description]:

- Pressing , the display becomes x.x.x.x.-F.

 F means file name. It is used to distinguish different data sets stored on a single cassette. It is also used to read back data. Press . and the display becomes x.x.x.x.-S. . S represents the starting address of the data to be written. Press . again and the display becomes x.x.x.x.-E.

 E represents the ending address of the data to be written. Before pressing , you must connect the microphone of the recorder to MIC jack of MPF-I and press PLAY and REC to start recording. If the recorder is not ready and you press data is still sent out. This data will not be recorded on tape. During transfer the display is blank, the TONE-OUT LED is on and a tone sounds.
- 2. For the setting data of 0000 OFFF in EPB-MPF equals to the data of 8000 8FFF in MPF-I, transfering the data of 0000 OFFF to tape in the EPB-MPF monitor is similar in transfering the data of 8000 8FFF to tape in the MPF-I monitor.

6.11	Reading	data	from	tape-	TAPE	Key

TAPE (filename) ω

Example: Read data from recorder, filemane is 1234, the tape is prepared by recorder.

Key	Display	Comments
	××××-E	;The initial state of EMP-MPF.
TAPE	×.×.×.•F	;F is the mnemonic of filename.
ec DE PL AF	1.2.3.4F	;Filename = 1234. Connect the recorder (using earphone jack) to the EAR jack in MPF-I.
ao		Start execution. The display is blank while EPB-MPF is searching for the filename.
(PLAY)	• • • • •	;Press PLAY of recorder. The recorder output volume should be turned to maximum.
	1 2 3 4 - F	EPB-MPF echoes the signal read from tape on its own speaker (if the volume is too low,
	40 - 40 - 50 - 30 - 30	then there will be no sound). Every file name read by the monitor will be displayed for
	××××.×.	1.5 seconds. When the desired file is found, '-' is changed into '-'. When finished, the last address read in is displayed.

[Description]:

Starting and ending addresses are already stored on the tape so there is no need to input them. The user just needs to input the file name. A check is also recorded on the tape which EPB-MPF will check when reading back. If not matched, the dispaly will be -Err .

If matched, the last input byte will be displayed.

If the data read from the tape are stored in a system stack, errors will occur. Care must be taken when you prepare tape data by The tape data are echoed on the MPF-I speaker, so it is very easy to determine whether the tape is empty or not. This allows you to check a tape before recording data on it, so you do not destroy data previously recorded.

- 2. The key in EPB-MPF monitor is to store the address of 8000 - 8FFF in RAM buffer onto tape through MPF-I. So, the data also read to the same address (8000 - 8FFF) in RAM when it is back from tape.
- 3. If the data of tape is 1800 lfff in RAM address which users want to store it onto the RAM buffer of EPB-MPF, they should read data by the we key in MPF-I monitor and then move this value to 8000 8FFF by using the command.
- 4. If the parameter has any errors in executing

 or key, the display will show

 Err. The above situation indicates the command returns to the MPF-I monitor, but the key has no any function under such situation. The command return to the MPF-I monitor just when EPT shows under the execution of and but the rest is still in the EPB-MPF monitor.

Example: EPROM blank test

Key	Dispaly	Comments
	$\times \times \times \times - E$;The initial state of EPB-MPF.
REAT	r E A d	;Set READ command. There is no EPROM on textool.
90		Execute READ command.
	PASS-r	READ finished. The data READ in are all FF.
YEAT FY	0.0.0.0. s	; Put the EPROM which you want to check onto textool.
90		;Execute VERIFY command.
	PASS-H	; If the EPROM is blank then the display is $PASS-H$, otherwise the display is $\times \times \times \times \times$ and stops at the first address. which is not equal.



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